

Docket No.: 0756-1531

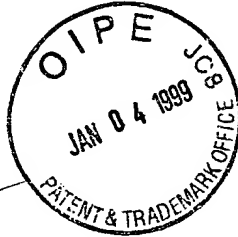
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of  
Shunpei YAMAZAKI et al.

Serial No.: 08/691,434

Filed: August 2, 1996

For: METHOD OF FABRICATING  
SEMICONDUCTOR DEVICES AND  
APPARATUS FOR PROCESSING A  
SEMICONDUCTOR



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) Art Unit: 2822  
) Examiner: M. Wilczewski  
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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Trademarks, 2900 Crystal Drive, Arlington, Virginia 22202-3513, on 12-30-98

RESPONSE

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The Official Action dated August 31, 1998 has been received and its contents carefully noted. Filed herewith is a *Request for One-Month Extension of Time*, which extends the period for response to December 31, 1998. Thus, it is respectfully submitted that this response is timely filed.

Claims 16, 56, and 74 are amended herewith and new claims 75-91 are added to complete the scope of protection to which applicant is entitled. New claims 75-91 are readable on the elected species. As amended, claims 16, 56 and 74 recite that doping is performed using a dopant impurity that is made into a plasma around a grid electrode and is accelerated toward the semiconductor layer by a voltage applied to an anode electrode. It is

respectfully submitted that the reference to Codama, as well as the other prior art of record, does not disclose or suggest this feature of the present invention. Specifically, Codama fails to suggest that a plasma of a dopant impurity is made around a grid electrode and the dopant impurity is accelerated by a voltage applied to an anode electrode. For this reason, it is submitted that the present invention is distinguished over the prior art of record and favorable reconsideration is requested.

Finally, submitted herewith is an Information Disclosure Statement citing U.S. Patent 5,286,296. This patent is related to JP 04-251921, which was previously submitted for consideration by the Examiner. Consideration of this reference, together with the previously submitted references, is requested in the following Official Action.

In view of the above comments, it is respectfully submitted that the present application is in condition for allowance. If the Examiner has any questions concerning this application, she is invited to contact the undersigned.

Respectfully submitted,



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